

St. John College of Engineering and Management

(A Christian Religious Minority Institution)

Approved by AICTE, Recognized by DTE and Affiliated to University of Mumbai and MSBTE
 St. John Technical Campus, Veeror, Manor Road, Palghar (E), District Palghar, -401404, Tel.No. (025252) 256486, 256792

NAAC Accredited with Grade A⁺ (2023-28)



TIME TABLE FOR SECOND YEAR BACHELOR OF ENGINEERING ECS SEMESTER-IV (DIV-A) 2023-2024

TIME	Monday	Tuesday	Wednesday	Thursday	Friday	FACULTY
08:45 am – 09:45 am	Electronic Circuits (SK)	Skill-base Lab Theory (VR)	A2: EM-IV TUT(MK) A1 & A3: Mini Proj.	A1: EM-IV TUT(MK) A2 & A3: Mini Proj.	Controls and Instrumentation (MNK)	PG: Dr. P Ghonge VR: Mrs. Vishakha Rane SK: Mrs. Shilpa Kare AC: Ms. Aishwarya Churi
09:45 am – 10:45 am	Engineering Maths IV (MK)	Microprocessors and Microcontrollers (AC)	Engineering Maths IV (MK)	Engineering Maths IV (MK)	Microprocessors and Microcontrollers (AC)	MK: Mr. Machhindra Khandare MNK: Mrs. Meenal Kate
10:45 am – 11:45 am	Controls and Instrumentation (MNK)	Engineering Maths IV (MK)	Electronic Circuits (SK)	Discrete Structures and Automata Theory (HP)	Discrete Structures and Automata Theory (HP)	HP: Mrs. Hiral Patel PJ: Mrs. Parul Jha
11:45 am – 12:45 pm	Microprocessors and Microcontrollers (AC)	Discrete Structures and Automata Theory (HP)	MENTORING	Controls and Instrumentation (MNK)	Electronic Circuits (SK)	JG: Mrs. Joslyn Grasius TP: Trusha Pimple
12:45 pm – 01:30 pm	LUNCH BREAK					CLASS ADVISOR Ms. Aishwarya Churi
01:30 pm – 02:30 pm	A1: ECL (SK) A2: Skill Lab (VR) A3: MPMCL (AC)	A1: MPMCL (JG) A2: CIL (MNK) A3: EC Lab (SK)	A1: Skill Lab (P) A2: ECL (SK) A3: CIL (MNK)	Mini Project Theory (AC)	A1: CIL (MNK) A2: MPMCL (JG) A3: Skill Lab (TP)	BATCHES Batch 1: 1 to 20 Batch 2: 21 to 40 Batch 3: 41 to 60
2:30 pm – 03:30 pm	A2: Skill Lab (VR) A1 & A3: Mini Proj.	MP – 1B (All batch)	A1: Skill Lab (P) A2 & A3: Mini Proj.	MP – 1B (All batch)	A3: Skill Lab (TP) A2 & A1: Mini Proj.	
03:30 pm – 04:30 pm						

With effect from 15th Jan, 2024

* Extra Lecture assigned

Mrs. Hiral Patel
Time Table In-charge

HOD

Dept of Electronics & Information
 St. John College of Engineering
 & Management Palghar - 401404



Dr. G.V. Mulgund
Principal