

Mrs. Shilpa Katre
Assistant Professor
Department of Electronics & Telecommunication

Email id: shilpa.katre@gmail.com



EDUCATIONAL QUALIFICATIONS

Sr. No	Qualification	Class	Year of Passing	University
1	M. Tech. (Electronics Engineering)	1st	2011	R.T.M. Nagpur University
2	B. E. (Electronics Engineering)	1st	2000	R.T.M. Nagpur University

EXPERIENCE

✓ **Academic Experience:**

Total Experience Degree: 12 Years

Sr. No	Organization	Designation	Duration
1	St. John College of Engineering & Management	Assistant Professor	4 th July 2022 till date
2	G H Raisoni College of Engineering, Nagpur	Assistant Professor	1 Year
3	Rajiv Gandhi College of Engineering & Research, Hingna Nagpur	Assistant Professor	6 Years
4	Yeshwantrao Chavan College of Engineering, Hingna Nagpur	Assistant Professor	4 years
5	Guru Nanak Institution of Engineering and Management Nagpur	Assistant Professor	8 months

MEMBERSHIP OF PROFESSIONAL BODIES

- ✓ Lifetime Membership of ISTE - LM -81030
- ✓ IAENG Membership - Membership no 150294

AREA OF INTEREST

- ✓ VLSI
- ✓ Digital Signal Processing

RESEARCH

✓ Journal Publications:

1. Published Paper titled “ Dam Water Level Altering and Monitoring System Using Iot with Machine Learning”, in International Journal of All Research Education & Scientific Methods (IJARESM) 2023.
2. Presented Paper titled “Review the Prediction of Air Pollution using Machine Learning Models” in 2nd International Conference on “Recent Advances in Materials, Manufacturing and Machine Learning (RAMMML-2023).
3. Published Paper titled “Design and Development of 3-D Printer” in IJSREM, International Journal of Scientific Research in Engineering Management 2023.
4. Presented Paper titled “IoT based Smart Traffic Control” in ICEASM-20 International Conference on Advances in Engineering, Science and Management 2020
5. Presented Paper titled “IoT base Healthcare Monitoring Systems” in IJFGCN International Journal of Future Generation Communication & Networking ISSN 2233- 7857 – 2019
6. Presented Paper titled “Challenges and Issues in Wireless Sensor Network- A Review ” in IRJET, International Journal of Engineering and Technology, Volume 5 - Issue 4 2018.
7. Presented Paper titled “Implementation A low power 50 nm CMOS design of frequency synthesizer” in international Conference at IEEE sponsored ETC Dept., Karpagam College,TN Feb 2016
8. Published Paper titled “Microcontroller based Automated Solution Filling Module ” in International Journal of Engineering And Technology IRJETI.ISSN:2395-0056 Vol 3,Issue 5, May 2016
9. Published Paper titled “Efficient Power Conservation using Adiabatic Subtractor” in international Journal of IJIREEICE.ISSN:2321-2004 Vol 3,Issue 3, Feb 2015
10. Published Paper titled “Implementation of Adiabatic Flip Flop and Sequential circuits based on CPAL circuits” in international Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering IJAREEIE.ISSN:2320-3765 Vol 3,Issue 2, Feb 2014.
11. Published Paper titled “Adaptive median filter for image enhancement “in International Journal of Engineering Science and Innovative Technology (IJESIT),ISSN:2319-5967, ISO 9001-2008, January-2013
12. Published Paper titled “Performance analysis of sequential circuits using reversible logic” in International Journal of Engineering Science and Innovative Technology (IJESIT) ISSN:2319-5967,ISO 9001-2008,January-2013.

✓ Conferences/ Proceedings:

1. Presented Paper titled “Implementation of Plasma speaker Audio Modulated Solid State Tesla Coil” in Spandan 2017-National Conference on advances in Engineering Technology and Applied Sciences at YCCE 2017.
2. Presented paper on “Preliminary results on ECG compression algorithm for low cost implementation of virtual patient” at National Conference on Emerging Trends in Computational Science(NCETCS-2013) , 8-9 March 2013.
3. 12. Presented paper on” Adiabatic Amplifier and Power Analysis of Different Adiabatic

Inverters”at International conference SOCPROS -2011 (Soft Computing for Problem Solving) at IIT Roorkee ,20-22 Dec 2011.

STTP/WORKSHOPS

✓ STTP/Workshops Participated:

Sr. No	Topic of STTP/Workshop	Organization	Duration
1	CMOS Mixed signal and RF VLSI Design	NMEICT ,Indian Institute of Technology Kharagpur	One week Workshop
2	Parallel Programming Techniques	RG CER, Nagpur	One day Workshop
3	Challenges in Teaching Learning	RG CER, Nagpur	One day Workshop
4	Embedded Systems and its applications	RG CER, Nagpur	One week STTP
5	Advance in VLSI and DSP Designs	YCCE, Nagpur	One week STTP
6	Signals and Systems	Indian Institute of Technology Kharagpur	Two week STTP
7	Analog Electronics	Indian Institute of Technology Kharagpur	Two-week STTP
8	Microwave Engineering and Measurements	YCCE, Nagpur	One day Workshop
9	Image Processing with Practical Application	YCCE, Nagpur	One day Workshop
10	Introduction to Research Methodologies	Indian Institute of Technology Bombay	Two-week STTP

✓ STTP/Workshops Conducted:

Sr. No	Topic of STTP/Workshop	Organization	Duration
1	Entrepreneurship Awareness Drive in collaboration with Cell, IIT Kharagpur	RG CER, Nagpur	One Day Workshop
2	Entrepreneurship Awareness Camp	MCED, Nagpur	3 Days workshop

3	One Week STTP On Recent Trends in Communication and Embedded Systems & its applications	GHRCE, Nagpur	One-week STTP
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EXPERT LECTURES/SEMINARS

- ✓ Conducted Expert Lecture on Mobile Communication for Final year students at RGCER, Nagpur.
- ✓ Conducted Expert Lecture on Analog Design Circuits for Third year students at RGCER, Nagpur.
- ✓ Conducted Expert Lecture on Digital Signal Processing for Third year students at RGCER, Nagpur.

NPTEL ONLINE COURSES

- Completed successfully 12 Week online NPTEL Course on ‘Microprocessor & Applications’ in Nov 2018

PROJECTS

- ✓ **Projects Guided :**
Guided more than 10 UG Projects.

Personal details:

Date of Birth : 31/07/1978
Gender : Female
Marital Status : Married
Nationality : Indian
Languages : Hindi, Marathi, English,

Residential Address:

B4/3, Adani Colony, ADTPS, Dahanu

Declaration:

I hereby declare that the above particulars are true to the best of my knowledge and belief.

Place: Palghar

Date:

Prof. Shilpa S Katre