



Date: 11/06/2024

Department of Electronics and Computer Science

1st Board of Studies (BoS) Meeting of Electronics and Computer Science Department conducted on
11th June 2024 (Tuesday) at 10.30 am

Minutes of the Meeting

Agenda

For approval

1. To review and discuss on Curriculum design, development, and Implementation w. r. t. NEP 2020 and Autonomy requirements
2. Approval of scheme SJCEM R-24 w.e.f. 2024-25 for Academic Year 2024-25 of B.E. Electronics and Computer Science Program
3. Academic Planning, preparation, execution, and knowledge management.
4. To discuss and review the Evaluation and Assessment methods.
5. Examiner Panel Approval.
6. Any other suggestions or feedback from members.

Following Members were present for BOS Meeting

Sr. No.	Name and Affiliation of BOS Members	Designation in BOS Committee	Mode of Presence in BOS Meeting
1.	Dr. Pandharinath Ghonge, Associate Professor & HOD, ECS and EXTC Dept St. John College of Engineering and Management, Palghar	BOS Chairman	Offline
2.	Dr. Deepak Bhoir Professor(ECS) Fr. Conceicao Rodrigues College of Engg, Bandra, Mumbai	VC Nominee, University of Mumbai	Online
3.	Dr. Sunil Dhore, Professor & Head(Computer Engineering), Army Institute of Technology, Dighi, Pune	Subject Expert 01 Outside the parent University	Online
4.	Dr. Sanjay Nalbalwar Professor & Head (EXTC), DBATU,Lonere	Subject Expert 02 Outside the parent University	Online
5.	Dr. Kushal Tuckley Managing Director, AGV Pvt. Ltd. Kalyan, Thane	Expert from Industry/corporate sector/allied areas	Online
6.	Mr. Prathamesh Patil Sr. Engineer AMD, Mumbai	Alumni Member	Online
7.	Dr. Dayakshini Sathis,	Experts from outside the	Online



Initially BoS Chairman Dr. Pandharinath Ghonge welcomed all the members and initiated the meeting. The agenda of the 1st BoS meeting were introduced to all the members.

Agenda 1

To review and discuss on Curriculum design, development, and Implementation w.r.t NEP 2020 and Autonomy requirements.

Resolution 1

The Scheme, Credit Structure based on NEP 2020 guidelines for new Autonomy Curriculum **SJCEM R-24** for Electronics and Computer Science Program was reviewed and resolved. (Total 173 Credits from first year to final year).

Agenda 2

Approval of scheme SJCEM R-24 w.e.f. 2024-25 for Academic Year 2024-25 of B.E. Electronics and Computer Science Program

Resolution 2

SJCEM got autonomy from AY 2024-25, all students enrolled in first year in AY 2023-24 learnt same subjects as common curriculum offered by University of Mumbai under scheme REV- 2019 'C' Scheme. They all have gathered 38 credits by studying same subjects.

It is resolved in BOS meeting that all second year (Autonomous) students can be offered a new curriculum to get advantage of NEP 2020 and industry relevance. With respect to that, second year (autonomous) new scheme and syllabus is designed which will be effective from AY 2024-25. Every year as the batch progresses to new academic year scheme and syllabus will be updated.

It was unanimously resolved to incorporate following points in the syllabus-

- a) Inclusion of Operating Systems Course in 2nd year.
- b) The DBMS course should be shifted to 3rd year with addition of advanced topics.

Following is the scheme for semester 3rd & 4th of Electronics and Computer Science Program

Program Structure for Second Year Electronics and Computer Science Semester III

Course Code	Ver-tical	Course Name	Contact Hrs			Credit Allot- ted			Total Cre- dits
			Th	Tut	Pr	Th	Tut	Pr	
24ECPCC301	PCC	Discrete Mathematics	3	1	0	3	1	0	4
24ECPCC302	PCC	Semiconductor Devices & Cir- cuits	2	0	2	2	0	1	3
24ECPCC303	PCC	Digital Electronics & Computer Organization	3	0	2	3	0	1	4
24ECPCC304	PCC	Data Structures & Algorithm	3	0	2	3	0	1	4
24ECPCC305	PCC	Professional Skills (C++&Java)	1	-	4	1	-	2	3
24ECAEC30 1	AEC	Basic Communication Skills – II	-	1	-	-	1	-	1
24ECVEC301	VEC	Universal Human Values	2	1	-	2	1	-	3
		Total	14	3	10	14	3	5	22

Course Code	Ver-tical	Course Name	Evaluation Scheme					
			IAE 1	IAE 2	ESE	CA	OR/ PR	Total
24ECPCC301	PCC	Discrete Mathematics	20	20	60	25	-	125
24ECPCC302	PCC	Semiconductor Devices & Cir- cuits	20	20	60	25	25	150
24ECPCC303	PCC	Digital Electronics & Computer Organization	20	20	60	25	25	150
24ECPCC304	PCC	Data Structures & Algorithm	20	20	60	25	25	150
24ECPCC305	PCC	Professional Skills (C++&Java)	10	10	30	25	25	100
24ECAEC30 1	AEC	Basic Communication Skills – II	-	-	-	25	-	25
24ECVEC301	VEC	Universal Human Values	10	10	30	25	-	75
		Total	100	100	300	175	100	775

Program Structure for Second Year Electronics and Computer Science Semester IV

Course Code	Vertical	Course Name	Contact Hrs			Credit Allotted			Total Credits
			Th	Tu	Pr	Th	Tu	Pr	
24ECPCC401	PCC	Statistical Analysis	3	-	-	3	-	-	3
24ECPCC402	PCC	Microprocessor and Microcontroller	3	-	2	3	-	1	4
24ECPCC403	PCC	Operating Systems	3	-	-	3	-	-	3
24ECPCC404	PCC	Computer Networks	3	-	2	3	-	1	4
24ECPCC405	PCC	Sensors Technology and Application	1	-	2	1	-	1	2
24ECAEC401	AEC	Employability Enhancement Program –3 (Communication)	-	-	2	-	-	1	1
24ECVSEC401	VSEC	Employability Enhancement Program - 3 (Technical)	1	-	2	1	-	1	2
24ECVSEC402	VSEC	Industry Certification	-	-	-	-	-	1	1
24ECINT401	OJT	Internship 1	-	-	-	-	-	2	2
Total			14	3	10	14	3	5	22

Course Code	Vertical	Course Name	Evaluation Scheme					Total
			IAE 1	IAE 2	ES E	C A	OR/ PR	
24ECPCC401	PCC	Statistical Analysis	20	20	60	25	-	125
24ECPCC402	PCC	Microprocessor and Microcontroller	20	20	60	25	25	150
24ECPCC403	PCC	Operating Systems	20	20	60	-	-	100
24ECPCC404	PCC	Computer Networks	20	20	60	25	25	150
24ECPCC405	PCC	Sensors Technology and Application	10	10	30	25	-	75
24ECAEC401	AEC	Employability Enhancement Program –3 (Communication)	-	-	-	25	-	25
24ECVSEC401	VSEC	Employability Enhancement Program - 3 (Technical)	10	10	30	25	-	75
24ECVSEC402	VSEC	Industry Certification	-	-	-	-	50	50
24ECINT401	INT	Internship 1	-	-	-	-	25	25
Total			100	100	300	175	100	775



The Electronics and Computer Science (ECS) program was introduced at the institute in the Academic Year 2022–2023. Currently, students are enrolled in the first to third years of the program.

As part of the institute's transition to autonomous status, the first and second year ECS students will follow the new autonomous curriculum (SJCEM R-24), designed in alignment with NEP 2020.

For the third-year batch, which has been following the University of Mumbai's (UoM) REV-2019 'C' Scheme, it has been resolved that:

- Since these students have already completed approximately 50% of their total program credits under UoM,
- The core academic structure—including theory and laboratory subjects—will not be modified for the third and fourth years.
- The total number of credits will remain unchanged, in order to preserve academic consistency and ensure degree recognition in accordance with UoM guidelines.

The Board of Studies (BOS) has further recommended that, in light of the National Education Policy (NEP) 2020 being implemented from the Academic Year 2024–25, and considering its enhanced alignment with industry expectations, the curriculum be enriched with the following key components:

- In 5th semester: professional communication and ethics-II of 2 credits can be replaced with employability enhancement program (technical) and Mini project- 2A of 2 credits can be replaced with Corporate Communication and Employability skill-I (1 credit) and Mini project IA (1 Credit).
- In 6th semester: Data Warehousing and Mining lab of 2 credits can be replaced with Employability Enhancement Program (technical)-II, and Mini Project -2B of 2 credits can be replaced with Industry Certification (1 credit) and Internship-II (1 credit).

Following is the modified scheme for third year Electronics and Computer Science

Program Structure for Third Year Electronics and Computer Science (With Effect from 2024-2025) Semester V

Course-Code	Ver-tical	Course Name	Contact Hrs			Credit Al-lotted			Total Cre-dits
			Th	Tut	Pr	Th	Tut	Pr	
24ECPCC501	PCC	Communication Engineering	3	-	2	3	-	1	4
24ECPCC502	PCC	Computer Organization and Architecture	3	-	-	3	-	-	3
24ECPCC503	PCC	Software Engineering	3	-	-	3	-	-	3
24ECPCC504	PCC	Web Technologies	3	-	2	3	-	1	4
24ECPEC501 X	PEC	Department Level Optional Course-I	3	-	2	3	-	1	4
24ECAEC50 1	AEC	Corporate Communication and Employability Skills-I	-	-	2	-	-	1	1
24ECVSEC50 1	VSEC	Employability Enhancement Program (Technical)	-	-	4	-	-	2	2
24ECPRJ501	PRJ	Mini Project 1A	-	-	2	-	-	1	1
Total			15	0	14	15	0	7	22

Course Code	Ver-tical	Course Name	Evaluation Scheme					
			IAE 1	IAE 2	ESE	CA	OR/PR	Total
24ECPCC501	PCC	Communication Engineering	20	20	60	25	25	150
24ECPCC502	PCC	Computer Organization and Architecture	20	20	60	-	-	100
24ECPCC503	PCC	Software Engineering	20	20	60	-	-	100
24ECPCC504	PCC	Web Technologies	20	20	60	25	25	150
24ECPEC501 X	PEC	Department Level Optional Course-I	20	20	60	25	25	150
24ECAEC50 1	AEC	Corporate Communication and Employability Skills-I	-	-	-	25	-	25
24ECVSEC50 1	VSEC	Employability Enhancement Program (Technical)	-	-	-	25	-	25
24ECPRJ501	PRJ	Mini Project 1A	-	-	-	25	25	50
Total			100	100	300	150	100	750

Program Structure for Second Year Electronics and Computer Science Semester VI

Course Code	Vertical	Course Name	Contact Hours			Credits Allotted			Total Credit
			T h	T u	P r	T h	T u	P r	Tot al
24ECPCC601	PCC	Embedded Systems and RTOS	3	-	2	3	-	1	4
24ECPCC602	PCC	Artificial Intelligence	3	-	2	3	-	1	4
24ECPCC603	PCC	Computer Networks	3	-	2	3	-	1	4
24ECPCC604	PCC	Data Warehousing and Mining	3	-	-	3	-	-	3
24ECPEC601	PEC	Department Level Optional Course –II	3	-	-	3	-	-	3
24CSVSE601	VSEC	Employability Enhancement Program (Technical)	-	-	4	-	-	2	2
24CSVSE602	VSEC	Industry Certification	-	-	-	-	-	1	1
24CSOJT601	OJT	Internship – II	-	-	-	-	-	1	1
Total			15	-	10	15	0	8	22

Course Code	Vertical	Course Name	Evaluation Scheme					Total
			Theory			Practical		
			IA E1	IAE 2	ESE	CA	O R/ PR	
24ECPCC601	PCC	Embedded Systems and RTOS	20	20	60	25	-	125
24ECPCC602	PCC	Artificial Intelligence	20	20	60	25	-	125
24ECPCC603	PCC	Computer Networks	20	20	60	25	25	150
24ECPCC604	PCC	Data Warehousing and Mining	20	20	60	25	-	125
24ECPEC601	PEC	Department Level Optional Course –II	20	20	60	-	-	100
24CSVSE601	VSEC	Employability Enhancement Program (Technical)	-	-	-	50	-	50
24CSVSE602	VSEC	Industry Certification	-	-	-	-	50	50
24CSOJT601	OJT	Internship – II	-	-	-	-	25	25
Total			100	100	100	100	300	750

Department Level Optional Course-2

Course Code	Department Level Optional Course-2
24ECPEC6011	Machine learning
24ECPEC6012	Industrial Automation
24ECPEC6013	Digital Signal Processing
24ECPEC6012	Electronic Product Design

Agenda 3: Academic Planning, preparation, execution, and knowledge management.

Resolution 3

All the members were given an idea about the Academic Planning and it was resolved that we will have a 15-week Academic Calendar and student's college timings will be from 9.00 am till 4.15pm from Monday to Friday and every first, third and fifth Saturday of the Month.

Agenda 4

To discuss and review the Evaluation and Assessment methods.

Resolution 4

Evaluation and assessment scheme is shown in the syllabus of respective subjects. Evaluation and Assessment methods was reviewed and confirmed. Panel of Examiners for theory and lab courses was reviewed and confirmed

Evaluation and Assessment Scheme:

A. Internal Assessment Examination (IAE):

Assessment consists of two class tests, each 20 marks. The IAE 1 will cover any three Course Outcomes (COs) and IAE 2 will cover the remaining three Course Outcomes (COs). Each test will have a duration of one hour.

B. End Semester Theory Examination (ESE):

End Semester exam of 60 Marks will be conducted based on the entire syllabus.

C. Continuous Assessment (CA) :

Continuous Assessment should consist of the following

Experiments / Tutorials (8 to 10): 10 marks (All COs / LOs should be covered)

Attendance (Theory & Practical): 05 marks

Teacher Assessment Examination (TAE): 10 Marks

Note: Number of activities to be conducted under TAE would be as per the subject need.

D. Oral & Practical Exam

Based on the entire syllabus, oral (10 marks) & practical/implementation (15 marks) examination will be conducted.

It was discussed and resolved that Examination will be conducted by SJCEM for all UG and Results will be declared by institute following which submission will be done to UoM as per guidelines for awarding degree to students.

Agenda 5: Examiner Panel Approval.

Examiner Panel was approved by the BOS members.

Examiner Panel

Semester- III

Course Code	Vertical	Course Name	Examiner 1	Examiner 2	Examiner 3
24ECPC C301	PCC	Discrete Mathematics	Mrs.Parul Jha 7219169833, parulj@sjcem.edu.in	Ms. Reshma Chavan, 98670 30907, reshmac@sjcem.edu.in	Mayur Gohil, VCET, Vasai 8355801862, mayurgohil@gmail.com
24ECPC C302	PCC	Semiconductor Devices & Circuits	Mrs. Shilpa Katre Shilpak@sjcem.edu.in	Mrs. Joslyn Gracias joslyng@sjcem.edu.in	Mr. NileshShimpi Atharva College of Engineering, Malad 7507789405 shimpi.nilesh001@gmail.com
24ECPC C303	PCC	Digital Electronics & Computer Organization	Dr. P A Ghonge, pandharinathg@sjcem.edu.in	Mr. Sachin Sase, Sachins@sjcem.edu.in	Mr.Bharat Warude VIVACollege, Virar 9987001925 bharatwarude@viva-technology.org
24ECPC C304	PCC	Data Structures & Algorithm	VishakhaRane vishakhar@sjcem.edu.in	Ms. Aishwarya Churi, SJCEM 9890413170, aishwaryach@sjcem.edu.in	Ms. Sonali Karthik Theem College, Boisar 8369393250 Sonali.karthik@theemcoe.org
24ECPC C305	PCC	Professional Skills (C++&Java)	Mrs. Hiral Patel, SJCEM, hiralp@sjcem.edu.in	Mrs. Joslyn Gracias, joslyng@sjcem.edu.in	Ms.Monika Pathare TheemCollege, Boisar 9890857548 Monika.pathare@rheemcoe.org
24ECAE C301	AEC	Basic Communication Skills – II	Ms. Suvarna More, SJCEM 7020885340 suvarnam@sjcem.edu.in	Dr. Eknath Patil, SJCEM 99211 18025, eknathp@sjcem.edu.in	Mr. Rishabh Melwanki, VCET, Vasai 9029353539 rishabhm2355@gmail.com
24ECVE C301	VEC	Universal Human Values	Ms. Beatrice Lobo, SJCEM 98216 64410, beatricel@sjcem.edu.in	Dr. Eknath Patil, SJCEM 99211 18025, eknathp@sjcem.edu.in	Mr. Rishabh Melwanki, VCET, Vasai 9029353539 rishabhm2355@gmail.com

Semester- IV

Course Code	Vertical	Course Name	Examiner 1	Examiner 2	Examiner 3
24ECPC C401	PCC	Statistical Analysis	Mrs. Parul Jha SJCEM 7219169833, pa- rulj@sjcem.edu. in	Ms. Reshma Cha- van, SJCEM 98670 30907, resh- mac@sjcem.edu.in	Mayur Gohil, VCET, Vasai 8355801862, mayurgo- hil@gmail.com
24ECPC C402	PCC	Microprocess or and Microcontroll er	Mrs. Joslyn Gracias, SJCEM 9158875875, jos- lyng@sjcem.ed u.in	Ms. Aishwarya Churi, SJCEM 9890413170, aishwaryach@sjcem. edu.in	Dr. Kevin Naronha, SFIT, Borivali 9870335301, kevin- naronha@sfit.ac.in
24ECPC C403	PCC	Operating Systems	Mr. Sachin Sase, SJCEM 7507376241, sa- chins@sjcem.ed u.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.i n	Dr. Nilesh Bhelar RGIT, Mumbai. 8149330733, nilesh.bhelkar@mctr git.ac.in
24ECPC C404	PCC	Computer Networks	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem. edu.in	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Dr. Vaqar Ansari, SFIT, Borivali 9987461239, vaqar@sfit.ac.in
24ECPC C405	PCC	Sensors Technology and Application	Dr. Pandhari- nath Ghonge, SJCEM, 9869025984, pandhari- nathg@sjcem.e du.in	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Dr. Kevin Naronha, SFIT, Borivali 9870335301, kevin- naronha@sfit.ac.in
24ECAE C401	AEC	Employability Enhancement Program –3 (Communication)	Mr. Beatrice Lobo, SJCEM 98216 64410, beat- ricel@sjcem.ed u.in	Dr. Eknath Patil, SJCEM 99211 18025, eknathp@sjcem.edu.i n	Mr. Rishabh Mel- wanki, VCET Vasai, rishabhm2355@gmai l.com
24ECVSEC 401	VSE C	Employability Enhancement Program - 3 (Technical)	Mr. Beatrice Lobo, SJCEM 98216 64410, beat- ricel@sjcem.ed u.in	Dr. Eknath Patil, SJCEM 99211 18025, eknathp@sjcem.edu.i n	Mr. Rishabh Mel- wanki, VCET Vasai, rishabhm2355@gmai l.com

Semester- V

24ECPC C501	PCC PCC	Communication Engineering	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Lynette Demello DJ Sanghvi college 8108118509
24ECPC C502	PCC	Computer Organization and Architecture	Ms. Aishwarya Churi, SJCEM 9890413170, aishwaryach@sjcem.edu.in	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Nilesh Shimpi Atharva College of Engineering, 7507789405 shimpi.nilesh001@gmail.com
24ECPC C503	PCC	Software Engineering	Mrs. Meenal Kate, 9516976233 meenalk@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Priti Rumao VCET, Vasai 7058438939 Priti.rumao@vcet.edu.in
24ECPC C504	PCC	Web Technologies	Mrs. Vishaka rane, SJCEM, 9730371605, vishakhar@sjcem.edu.in	Mrs. Meenal Kate, 9516976233 meenalk@sjcem.edu.in	Dipti Kale Asst. Prof DJSanghvi college 9819811982 Dipti.kale.djsce@ac.in
24ECPEC 501X	PEC	Department Level Optional Course-I	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Lynette Demello DJ Sanghvi college 8108118509
24ECAE C501	AE C	Corporate Communication and Employability Skills-I	Dr. Eknath Patil, SJCEM 99211 18025, eknathp@sjcem.edu.in	Mr. Beatrice Lobo, SJCEM 98216 64410, beatricel@sjcem.edu.in	Dr. Joseph Rodrigues, Asst. Prof. Fr. Agnel. Bandra
24ECVS EC501	VSE C	Employability Enhancement Program (Technical)	Ms. Aishwarya Churi, SJCEM 9890413170, aishwaryach@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Mr. Rishabh Melwanki, VCET Vasai, rishabh2355@gmail.com
24ECPRJ 501	PRJ	Mini Project 1A	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Nilesh Shimpi Atharva College of Engineering 7507789405 shimpi.nilesh001@gmail.com

Semester- VI

Course Code	Vertical	Course Name	Examiner 1	Examiner 2	Examiner 3
24ECPCC 601	PCC	Embedded Systems and RTOS	Mr. Sachin Sase, SJCEM 7507376241, sachins@sjcem.edu.in	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Nilesh Shimpi Atharva College of Engineering, 7507789405 shimpi.nilesh001@gmail.com
24ECPCC 602	PCC	Artificial Intelligence	Mrs. Vishakharane, SJCEM, 9730371605, vishakhar@sjcem.edu.in	Mrs. Meenal Kate, 9516976233 meenalk@sjcem.edu.in	Priti Rumao VCET, Vasai 7058438939 Priti.rumao@vcet.edu.in
24ECPCC 603	PCC	Computer Networks	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Dr. Vaqar Ansari, SFIT, Borivali 9987461239, vaqar@sfit.ac.in
24ECPCC 604	PCC	Data Warehousing and Mining	Ms. Aishwarya Churi, SJCEM 9890413170, aishwaryach@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Priti Rumao VCET, Vasai 7058438939 Priti.rumao@vcet.edu.in
24ECPEC 601	PEC	Department Level Optional Course –II	Dr. Pandharinath Ghonge, SJCEM, 9869025984, pandharinathg@sjcem.edu.in	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Mr. Bharat Warude VIVA College, 9987001925 bharatwarude@viva-technology.org
24CSVSE 601	VSEC	Employability Enhancement Program (Technical)	Dr. Pandharinath Ghonge, SJCEM, 9869025984, pandharinathg@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in	Mr. Rishabh Melwanki, VCET rishabhm2355@gmail.com
24CSVSE 602	VSEC	Industry Certification	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in
24CSOJT 601	OJT	Internship – II	Mrs. Hiral Patel, SJCEM 9173020762, hiralp@sjcem.edu.in	Mrs. Joslyn Gracias, SJCEM 9158875875, joslyng@sjcem.edu.in	Mr. Sandip Patil, SJCEM 9860823842, sandipp@sjcem.edu.in



Agenda 5: Any other suggestions or feedback from members.

To review and discuss improvements required for effective teaching learning process, project quality, and semester conduct w.r.t reports received from Academic Performance Audit, NBA, and NAAC.

Resolution 5

Improvements required for effective teaching learning process, project quality, and semester conduct w.r.t reports received from Academic Performance Audit, NBA, and NAAC is discussed and it is resolved that project based learning should improve with problem statements from sustainable development goals.

Following points were proposed for consideration once the Autonomy Processes are matured.

- I. Upgrade the workshop syllabus of First Year to include computer hardware, basics networking and PCB design, testing and operations of electronics laboratory equipment.
- II. Research internship should be offered at institute for student of 8th Semester.

Meeting was concluded with vote of thanks by BoS Chairman, Dr. Pandharinath Ghonge.

BOS Chairman
Electronics and Computer Science
Department