



**End Semester Supplementary Examinations
Time Table -A.Y 2025-26
Electronics and Computer Science**

**Semester: III
Scheme: SJCEM R-24B**

Day and Date	Time	Course Code	Course Name
Tuesday 16 th December 2025	10.00am to 12.00pm	24ECPCC302B	Semiconductor Devices & Circuits
Wednesday 17 th December 2025	10.00am to 12.00pm	24ECPCC303B	Digital Electronics & Computer Organization
Thursday 18 th December 2025	10.00am to 12.00pm	24ECPCC304B	Data Structures & Algorithm

**Prepared By
Exam Cell Incharge**

Deputy COE

Controller of Examination

Principal





St. John College of Engineering and Management

Autonomous Institute

(A Christian Religious Minority Institution)

Approved by AICTE and DTE, Affiliated to University of Mumbai / MSBTE

DTE Code : 3218 AICTE Permanent ID : 1-4790201

NAAC Accredited with Grade 'A+', Three Programs NBA Accredited



**End Semester Supplementary Examinations
Time Table A.Y 2025-26
Electronics and Computer Science**

**Semester: V
Scheme: SJCEM R-24B**

Day and Date	Time	Course Code	Course Name
Monday 15 th December 2025	01.30pm to 03.30pm	24ECPEC5022B	Semiconductor and VLSI Design

**Prepared By
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Deputy COE

Controller of Examination

Principal

