

Department of Electronics and Computer Science Time Table of Internal Assessment Examination 2025-26 (Supplementary)

This is to inform all Second /Third Year/Final Year students and the concerned teaching staff that the Internal Assessment Examination-II will be held on 11th to 12th Dec 2025.

The time slot of various subject shall be as follows

Day and Date	Time	Class	Course Code	Course Name
Thursday 11/12/2025	9.30 TO 11.00	S.Tech	24ECPCC301B	Discrete Mathematics
		B.E	24ECPCC701B	VLSI Design
		TE	24ECPCC501B	Image Processing and Machine Vision
	12.00 TO 1.30	S.Tech	24ECPCC303B	Digital Electronics and Computer Organization
		TE	24ECPCC502B	Advanced Database Management Systems
Friday 12/12/25	9.30 to 11.00	S.Tech	24ECPCC304B	Data Structure & Algorithm
		TE	24ECPEC5013B	Software Engineering and Quality Testing
	12.00 to 1.30	S.Tech	24ECPCC302B	Semiconductor Devices & Circuits
		TE	24ECPEC5021B	Internet Communication and Wireless Networks


Prepared By


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